

DataIO UniSite 82S100 Dumping V1.00

DataIO UniSite Programmable Logic Binary Format

The DataIO File Operations->Save File function outputs a binary file. The UniSite User Manual has some notes about using this raw format with logic devices:

“Note: The More/File Operations/Load File command is a transfer for an absolute binary format. The programmer must receive a JEDEC format for logic devices.”

“A saved file is stored in RAM Image Binary format. If you want to store a file in some other format, use the Transfer Data/Output to Disk command.”

That said, the programmer doesn't prohibit the Save File of a logic device in RIB format and in the simple case of a read & save of an 82S100 the resulting format is as follows:

<u>Byte Offset</u>	<u>Description</u>
0 ... 3	: <4 bytes, 32-bit number, matches file size, 250>
4 ... 7	: <4 bytes, 32-bit number, matches file size, 250>
8 ... 248	: <fuse map>
249	: <1 byte, NULL 0x00 terminator>

MAME Programmable Logic Binary Format

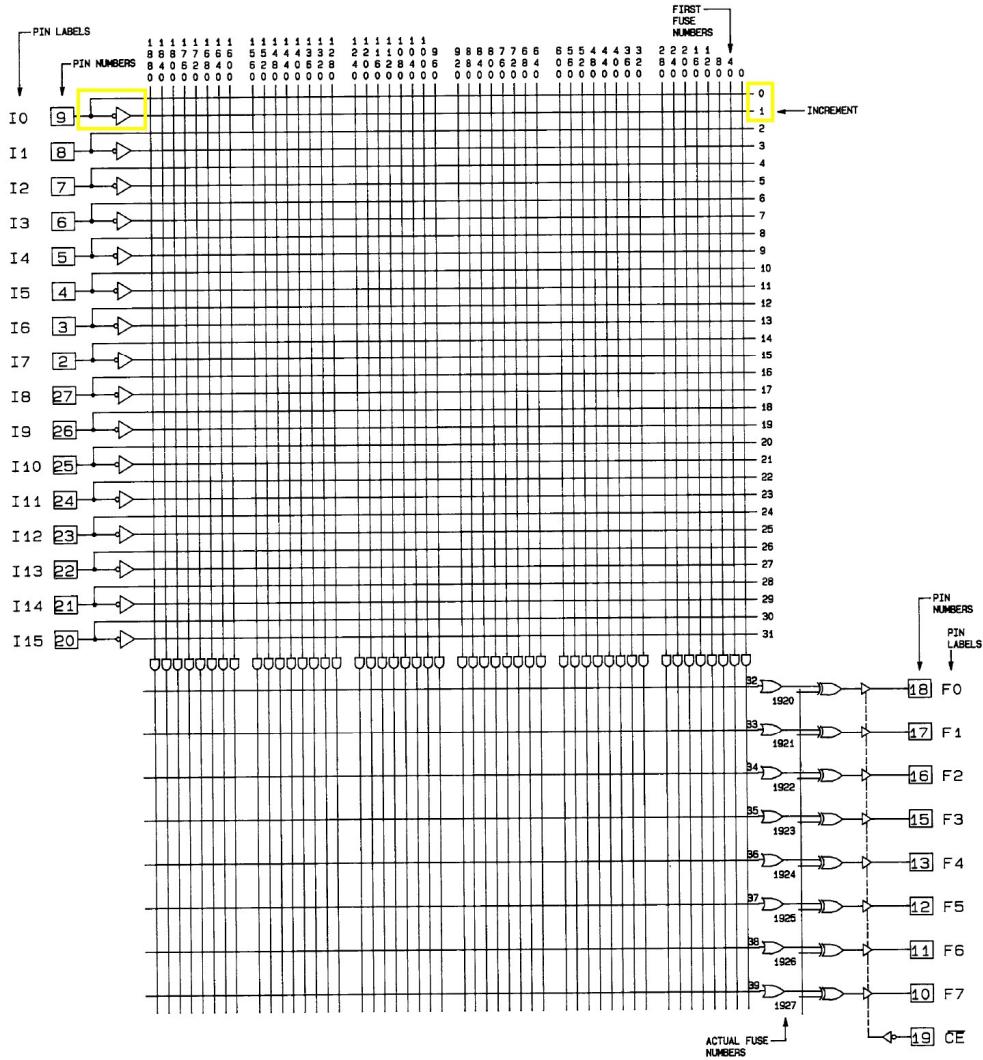
The logic device binary format used in MAME is documented in the source code for Jedutil:

<u>Byte Offset</u>	<u>Description</u>
0 ... 3	: <4 bytes, 32-bit number, number of fuses, 1928>
4 ... 244	: <fuse map>

Dumping Using JEDEC File Transfer

The resulting UniSite V5.80 “standard” JEDEC output from the 82S100 read does not match the MAME JEDEC output from Jedutil and the conversion of the Data IO JEDEC file to MAME binary format yields incorrect data. The DataIO fuse map for the 82S100 is documented on page 43 of the “981-0251_ProgrammableLogicDiagramPackage_Sep90” manual for which the general layout of the fuse map matches Jedutil but the none-inverting/inverting “I” inputs are swapped (i.e., I0 & ~I0, I1 & ~I1 etc.) as highlighted in the diagram below. It's not known why the DataIO dump is swapped.

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NOTE: FUSE NUMBER = FIRST FUSE NUMBER + INCREMENT

In binary this difference in fuse order corresponds to a swap transformation on the 4 bytes of the AND matrix thus:

Byte Offset	Description
0 ... 3	: 01234567 -> 10325476
4	: 01234567 -> no change
5 ... 8	: 01234567 -> 10325476
...	

There is a hack of MAME's Jedutil to perform this transformation available:

<https://github.com/prswan/arduino-mega-ict/blob/47f0c2b90bdee2b3790468ef90444a05343afe8e/utilities/82S100/jedparse.cpp#L176>

```
for ( ; cursrc < srcend; cursrc++)
    if (*cursrc == '0' || *cursrc == '1')
    {
        uint32_t jedcurfuse = curfuse;

        /* Special handling to correct the fuse order from the
         * DataIO UnitSite read of an 82S100. The fuse order
         * translation is 01234567 => 10325476 applied to the
         * AND matrix block (4 bytes of 5). Thus, the lower
         * bit of the fuse address is inverted.

        0000000E: A5 5A
        0000000F: FE FD
        00000013: A9 56
        00000014: FD FE
        00000018: A6 59
    */
    if (data->dataio_swap)
    {
        if ((jedcurfuse < 1920) &&
            ((jedcurfuse % 40) < 32))
        {
            jedcurfuse = curfuse & ~0x1UL;
            jedcurfuse |= (curfuse & 0x1) ^ 1;
        }
    }

    jed_set_fuse(data, jedcurfuse, *cursrc - '0');
    if (LOG_PARSE) printf("  fuse %u = %d\n", curfuse, 0);
    if (curfuse >= data->numfuses)
        data->numfuses = curfuse + 1;
    curfuse++;
}
```

Reference DataIO Dump of Zaccaria Cat'n Mouse IC 10M

Data I/O*

QP0028* QF1928* GO *

L0000

11111111011111110100111111111111
1101101011111111011111101101011
11111111011101111111111111111111
01100111101111110010010011111111
1111111100110111111111111111110110
1111111101111111001011111111111111
01101101111111111111111111111111
1111111100111101111111111111111111
0101011111111111111111111111111111
1111111101110111111111111111111111
L0320

11111111111111110110011101110110
01001001111111111111111101100111
01111001001001011111111111111111
0110011101110101001001001011111111
1110111110101011111111111111111111
1111111111111111011111111111111111
1111011101111111111111111111111111
1111111111111111111111111111111111
10101001111111110110110100000000
0000000000000000000000000000000000*

L0640

1111111111111111111111111111111111
1111111111111111111111111111111111
1111111111111111111111111111111111
1111111111111111111111111111111111
1111111111111111111111111111111111
1111111111111111111111111111111111
1111111111111111111111111111111111
1111111111111111111111111111111111
0000000000000000000000000000000000
0000000000000000000000000000000000*

L0960

01101001110101011010101111111111
01001001100110011101010110101011
11111111001001000101100111010101
101010111111110110110110010101
11010101101010111111111100100100
1001010111010101101010111111111
11011011010101110101011010101111
1111111110110110000000000000000000
0000000000000000000000000000000000
0000000000000000000000000000000000*

L1280

01101010110101011010101111111111
11011011100110101101010110101011
11111111011010101011010110101011
101010111111110010010001100110
11010101101010111111111100100100
100101011010101101010111111111
01001001010101101101010111111111
1111111110011000000000000000000000
0000000000000000000000000000000000
0000000000000000000000000000000000*

L1600

11111111011111110100111111111111
0100100111111110111111101101011
11111111011011010111111111111111
11111111111111111111111111111111
01111111100101111111111111111111
0000000000000000000000000000000000
0000000000000000000000000000000000
0000000000000000000000000000000000
0000000000000000000000000000000000*

L1920

00000000*

C9834*

Reference MAME Dump of Zaccaria Cat'n Mouse IC 10M